

REMARKS/ARGUMENTS

In view of the above amendments and remarks presented herein, favorable consideration and allowance of this application are respectfully requested.

Applicant's claims 1-17 are amended above to correct informalities noted by the Examiner. Applicant's claims are also amended to obviate the Examiner's rejections under 35 U.S.C. § 112, second paragraph, and to more clearly and distinctly set forth novel and patentable features of at least one non-limiting exemplary implementation disclosed in Applicant's patent application specification. The Abstract is re-written as suggested by the Examiner in the foregoing office action to more clearly summarize the technical disclosures of Applicant's patent application specification. Claims 4 and 11 are canceled without prejudice or disclaimer. New claim 32 has been added. The claim amendments, re-written Abstract and newly added claim are fully supported by the original disclosure and, thus, no new matter has been added. If the Examiner should disagree, however, it is respectfully requested that the challenged amendments/limitations be pointed out with particularity in the next Action so that support may be cited in response.

Applicant respectfully submits that all claims as presented and presently amended clearly distinguish over Miki (U.S. Patent 6,810,474) and/or Miki in view of Huang (U.S. Patent 4,943,909) for at least the following reasons:

Neither Miki nor Huang nor any other prior art reference of record, considered either alone or together, teach or suggest a data processing apparatus for speeding up execution of program instructions read from main memory where the data processing apparatus includes an instruction region storage section comprising content addressable (CAM) and random access (RAM) memory portions operatively configured as a "reuse table" which is separate from the

main memory, as set forth in Applicant's independent claims. Moreover, neither Miki nor Huang nor any other prior art reference of record, considered either alone or together, teach or suggest the use of an instruction stream processor having a "reuse window" portion comprising an I/O group data generator that uses a two-dimensional (2D) matrix-arranged memory for indicating dependency relations information in the manner as set forth in Applicant's independent claims.

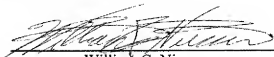
Newly added claim 32 is directed toward the same subject matter of claims 1-7 and includes recitation of the novel and patentable features of the 2D matrix-arranged memory elements of the dependency relation information storage memory and an instruction reuse table comprising CAM and RAM memory portions, neither of which are disclosed or suggested by Miki or Huang or any other prior art reference of record.

All objections and rejections raised in the 1/13/2011 Office Action have been addressed by the present Amendment and it is respectfully submitted that the present application is now in condition for allowance of all claims. Should there be any outstanding matters that need to be resolved, the Examiner is respectfully requested to contact Applicant's undersigned representative, using the telephone number listed below the signature line, to conduct an interview in an effort to expedite prosecution in connection with the present application.

The Commissioner is authorized to charge the undersigned's deposit account #14-1140 in whatever amount is necessary for entry of these papers and the continued pendency of the captioned application.

Respectfully submitted,

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